



PRODUCT AND PROCESS CHANGE NOTIFICATION

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ISSUE DATE: 17-Apr-2015
NOTIFICATION: 16734
TITLE: MC33931 Cold Temperature Test Elimination
EFFECTIVE DATE: 01-Jun-2015

DEVICE(S)

MPN
MC33931EK
MC33931EKR2
MC33931VW
MC33931VWR2

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AFFECTED CHANGE CATEGORIES

- TEST PROCESS

DESCRIPTION OF CHANGE

Freescale Semiconductor announces the cold temperature test elimination from the Final Test production flow for the MC33931 devices associated with this notification. Cold test performance is covered by the remaining Final Test production flow. The cold test elimination has been successfully qualified according to Freescale specifications.

Current Production Final Test Flow:
Hot – Cold

Proposed Production Final Test Flow:
Hot

REASON FOR CHANGE

Improve capacity for optimized cycle time and delivery.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No change to form, fit, function or reliability.

According to JEDEC Standard JESD46, lack of acknowledgement of this PCN within 30 days will be considered acceptance of change. To request further data or inquire about the notification, please enter a [Service Request](#).

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 09-Jan-2015

QUALIFICATION STATUS: COMPLETED

QUALIFICATION PLAN:

Freescale Semiconductor standard specification for Test Elimination was followed for this change. See attached Qualification Report.

RELIABILITY DATA SUMMARY:

Product is fully qualified. No further reliability data is needed for this change.

ELECTRICAL CHARACTERISTIC SUMMARY:

No change to Electrical Distributions.

CHANGED PART IDENTIFICATION:

There is no change to orderable part number or marking.

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[16734 Merlot SOIC HSOP Cold Temp Elimination Qualification Results.pdf](#)

[16734 Merlot SOIC HSOP Cold Temperature Elimination Delta Qualification Matrix.pdf](#)
