

M54455EVBE Errata

by: Microcontroller Division

This document identifies implementation differences between the M54455EVBE and the description contained in the following documents:

- *M54455 EVB Users Manual*

Refer to <http://www.freescale.com/coldfire> for the latest updates.

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PCI_REQ3/ATA_INTRQ Pull Resistor Contention

The following table summarizes errata on the M54455EVB.

Table 1. MCF5445x Errata Summary

Errata ID	Module Affected	Date Errata Added	Errata Title
1	PCI/ATA	11/07/07	PCI_REQ3/ATA_INTRQ Pull Resistor Contention

1 PCI_REQ3/ATA_INTRQ Pull Resistor Contention

1.1 Description

The PCI_REQ3 and ATA_INTRQ signals are multiplexed on the same pin on the MCF5445x devices. On the M54455EVB, this signal is used in the PCI and ATA interfaces. However, there are opposing pull resistors applied to this signal (RP4.3 and R904) causing undesirable effects.

1.2 Workaround

This signal should be routed to the PCI interface or ATA interface, not both.

- To use this signal for the ATA interface, cut the trace between pins 1 and 2 of the cut-trace option, CT9.
- To use this signal for the PCI interface, remove the R904 resistor.

1.3 Status

Revision 3 of the M54455EVB will provide a jumper allowing this signal to be routed to one interface or the other.

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